IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Jack A. Mandelman, et al. Examiner: Monica Lewis

Serial No: 10/812,283 Art Unit: 2822

Filed: March 29, 2004 Docket: FIS920000224US2 (13814AZ)

For: METHOD FOR FORMING DUAL Date: June 4, 2008

WORK-FUNCTION HIGH-PERFORMANCE SUPPORT MOSFETs IN EDRAM ARRAYS

Confirmation No.: 2253

Commissioner for Patents United States Patent and Trademark Office Alexandria, VA 22313-1450

RESPONSE UNDER 37 C.F.R. §1.111

Sir:

In response to the Office Action dated May 9, 2008, Applicants submit the following remarks for entry of record in the above-identified patent application.

CERTIFICATE OF ELECTRONIC FILING

I hereby certify that this correspondence is being deposited with the United States Patent & Trademark Office via Electronic Filing through the United States Patent and Trademark Office e-business website, on the date shown below.

Dated: June 4, 2008

Harry Andrew Hild Jr. Registration No. 51,803